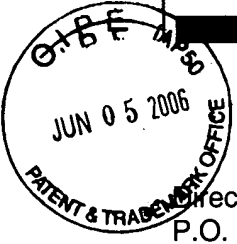


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Re: Patent No. 7,024,577
Issued: April 4, 2006
Inventors: Masahiro Nagata
For: PROGRAM LOGIC DEVICE FOR SYNCHRONOUS OPERATION
WITH MULTIPLE CLOCK SIGNALS
Attorney Docket No. 6340-000018

Dear Sir:

We have noted in proofreading Patent No. 7,024,577 the following errors:

- Column 3, line 62 (application page 7, line 15), after "invention;", delete "and".
- Column 3, line 64 (application page 7, line 17), after "FIG. 8", delete "." and insert --and--.
- Column 5, line 8 (application page 10, line 8), "maybe" should be --may be--.
- Column 6, line 57 (application page 14, line 11), "wait clk" should be --wait_clk--.
- Column 7, line 65 (application page 17, line 1), "carries" should be --carry--.

Kindly make these errors of record in your files.

Very truly yours,

W.R. Duke Taylor
Reg. No. 31,306

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